

# MWSCAS 2018 Symposium-at-a-Glance

Sunday 5 August 2018

07:00-15:00	Registration in the hallway			
	Mercuri	Martis	Luna	Saturni
08:30-10:00	T1: Tiny Inductively Powered Battery Chargers	T2: Neuromorphic Computing: Algorithms, Devices, and Circuits	T3: High Throughput CMOS Sensing Platforms for Life Science Applications	T4: Trusted Microelectronics: Devices, Circuits, and Systems
10:00-10:30	Coffee Break in the hallway			
10:30-12:00	T1: Continue	T2: Continue	T3: Continue	T4: Continue
12:00-13:00	Break			
13:00-14:30	T5: Design Methodology and Circuit Techniques for Any-Load Stable LDO Regulators with Instant Load Control	T6: Single- and Multi-Cell Battery Charging and Management Systems for IoT and Automotive Applications	T7: Portable and Scalable High Power Analog Mixed Signal Circuit Design For Automotive Applications in BiCMOS Processes	T8: Piezoelectric Energy Harvesting: Modeling, Design, Fabrication, and Testing
14:30-15:00	Coffee Break			
15:00-16:30	T5: Continue	T6: Continue	T7: Continue	T8: Continue
18:30-20:00	Welcome Reception in Augustus I			

Monday 6 August 2018

07:00-15:00	Registration in the hallway				
07:00-08:30	Breakfast in Augustus I				
	Mercuri	Martis	Luna	Saturni	Poster Area
08:30-10:00	A1L-A Communication Circuits and Systems	A1L-B VLSI Low Power Digital Circuits	A1L-C Bioengineering Systems and Biochip	A1L-D Hardware Implementation of Neural Network and Neuromorphic Computing	A2P-E Analog Design Applications
10:00-10:30	Coffee Break in the hallway				
10:30-10:50	Opening and Welcome, Augustus II				
11:00-12:00	Distinguished Keynote Speaker-Dr. Yervant Zorian, Augustus II				
12:00-13:30	Lunch in Augustus I				
13:30-15:00	A3L-A Special Session on Computer Arithmetic in Honor of Drs. Graham Jullien and William Miller – Part I	A3L-B CMOS Implementation of Analog Circuits	A3L-C Wireless Communications	A3L-D Image Processing I	A4P-E Analog Circuit Designs
15:00-15:30	Coffee Break				
15:30-17:00	A5L-A Special Session on Computer Arithmetic in Honor of Drs. Graham Jullien and William Miller – Part II	A5L-B Oscillators and Modulators	A5L-C Image Processing II	A5L-D Control I	Student Paper Contest
19:00	Steering Committee Meeting				

Tuesday 7 August 2018

07:00-15:00	Registration in the hallway				
07:00-08:30	Breakfast in Augustus I				
	Mercuri	Martis	Luna	Saturni	Poster Area
08:30-10:00	B1L-A Analog to Digital Convertors	B1L-B VLSI Digital Integrated Circuits I	B1L-C Energy Harvesting	B1L-D Control II	B2P-E Circuits and Systems I B2P-F Live Demos & M.Sc./PhD Forum
10:00-10:30	Coffee Break in the hallway				
10:30-11:30	Distinguished Keynote Speaker-Dr. Eby G. Freidman, Augustus II				
11:30-12:00	Distinguished Keynote Speaker-Dr. Mark Elo, Augustus II				
12:00-13:30	Lunch in Augustus I & Keynote on Women in Engineering – Dr. Carla Purdy				
13:30-15:00	B3L-A Low Power Design and Modeling	B3L-B VLSI Digital Arithmetic Circuits	B3L-C Power Convertors	B3L-D Design, Modelling and Fabrication of Semiconductor Technologies	B4P-E Signal Processing Systems
15:00-15:30	Coffee Break				
15:30-17:00	B6L-A Low Power and Low-Density Communication Systems	B6L-B VLSI Digital Integrated Circuits II	B6L-C Digital Signal Processing for Medical Applications	B6L-D Diagnostics & Fault Tolerant Control System I	B5P-E System Architecture, Hardware, Software Co-design
18:30	Banquet in Augustus I				

Wednesday 8 August 2018

07:00-15:00	Registration in the hallway			
07:00-08:30	Breakfast in Augustus I			
	Mercuri	Martis	Luna	Poster Area
08:30-10:00	C1L-A Digital Signal Processing	C1L-B Hardware and Cyber Security I	C1L-C Power	C2P-E Circuits and Systems II
10:00-10:30	Coffee Break in the hallway			
10:30-12:00	Distinguished Keynote Speaker- Sebastian Fischmeister, Augustus II			
12:00-13:30	Lunch & Awards Ceremony in Augustus I			
13:30-15:00	C3L-A Digital Filter Design	C3L-B Hardware and Cyber Security II	C3L-C Advanced Internet of Vehicles and Internet of Things	C4P-E Power Management and Hardware Security
15:00-15:30	Coffee Break			
15:30-17:00	C5L-A Communication Systems	C5L-B Reconfigurable Architecture	C5L-C Diagnostics & Fault Tolerant Control System II	C6P-E Late Breaking News